

**Amendments to the Specification:**

Please add the following new paragraph after paragraph [0023].

[0028.1] Fig. 10 is a schematic diagram of a five-element stack, with an optional signal phase delay element, such as an inductor, for example, in another embodiment of the invention.

Please add the following new paragraph after paragraph [0049].

[0049.1] FIG. 10 is a circuit diagram of another embodiment of the present invention. The diagram illustrates one way in which five piezoelectric elements 150 may be electrically connected together. Although the piezoelectric elements 150 are similar to each other, they are not necessarily identical. The segments of piezoelectric material 130 may be of increasing size and the capacitors 140 may be selected to correspond to the particular segment of piezoelectric material 130. An example of such an arrangement is described in FIGS. 7A, 7B, and 7C, described above. Referring again to FIG. 10, each piezoelectric element 150 may include a bridge rectifier 120. The bridge rectifier 120 may, for example, be a full-wave rectifier including four diodes 110. The bridge rectifier 120 may be connected to the piezoelectric material 130, and may be connected to a capacitor 140. Each piezoelectric element 150 may also include a signal phase delay element, such as an inductor 180, provided between each bridge rectifier 120 and said capacitive element. A stack of piezoelectric elements 150 may be connected electrically by connecting their capacitors 140 in series. One terminal of one of the capacitors 140 may be provided as a sensor output 170, and another may be connected to ground 160. It may be observed that a four-element stack may be created by removing the connection between the bottommost piezoelectric element 150 and instead connecting directly to ground.